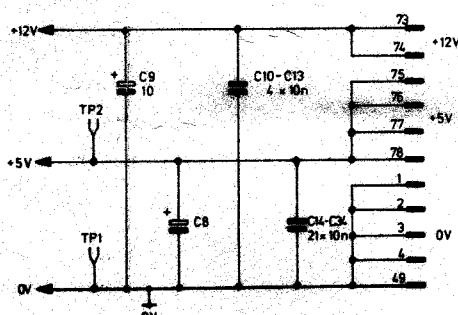
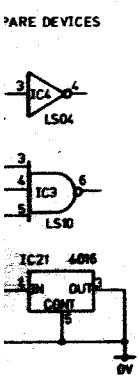


CHANGES		DATE	ISS
FIRST DRAWN		JAN 82	1

* C1 SHOULD BE OMITTED FOR USE WITH DRIVES NOT FITTED WITH A HEAD LOAD SOLENOID.

NOTE: FREQUENCY INFORMATION APPLIES TO 5 1/4" DISCS. C1 AND C3 VALUES MAY VARY - SEE HANDBOOK



THIRD ANGLE PROJECTION
 DO NOT SCALE
 DIMENSIONS IN MM
 LIMITS UNLESS OTHERWISE STATED
 DIMENSIONS WITH:
 NO DECIMAL = ±0.25
 ONE DECIMAL = ±0.125
 TWO DECIMALS = ±0.05
 ANGLE = ±0.5°
 FOR INTERPRETATION OF GEOMETRIC TOLERANCE SYMBOLS SEE LUCAS STANDARD No 51-3-5
 IF IN DOUBT, ASK

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PARTS LIST No. 9929-1148	ISSUE 1	DATE JAN 82	DRY PER SET	TITLE NASCOM
CIRCUIT No. 9929-1141	ISSUE 1	DATE JAN 82	FINISH	FLOPPY DISC CONTROLLER
ART-WORK No. 9929-1142 to 1145	ISSUE 1	DATE JAN 82	CHKD GCP	CIRCUIT DIAGRAM
			APPR C. B. G.	PROJ No.
				JOB No.
				ORIGINAL SCALE

Lucas
 Lucas Logic Limited
 Welton Road
 Wedgcock Industrial Estate
 Warwick CV34 5PZ
 9929-1147
 SHEET 1 OF 1
 DRG No. 9929-1141

78W CONN
PL 3

1E0 20
1E1 19
BA0 17
BA1 16

DB0 50
DB1 51
DB2 52
DB3 53
DB4 54
DB5 55
DB6 56
DB7 57

SYSTEM 5
CLOCK

RESET 14

DB DR 13

ST0 12

A0 30

A1 31

A2 32

A4 34

A3 33

A7 37

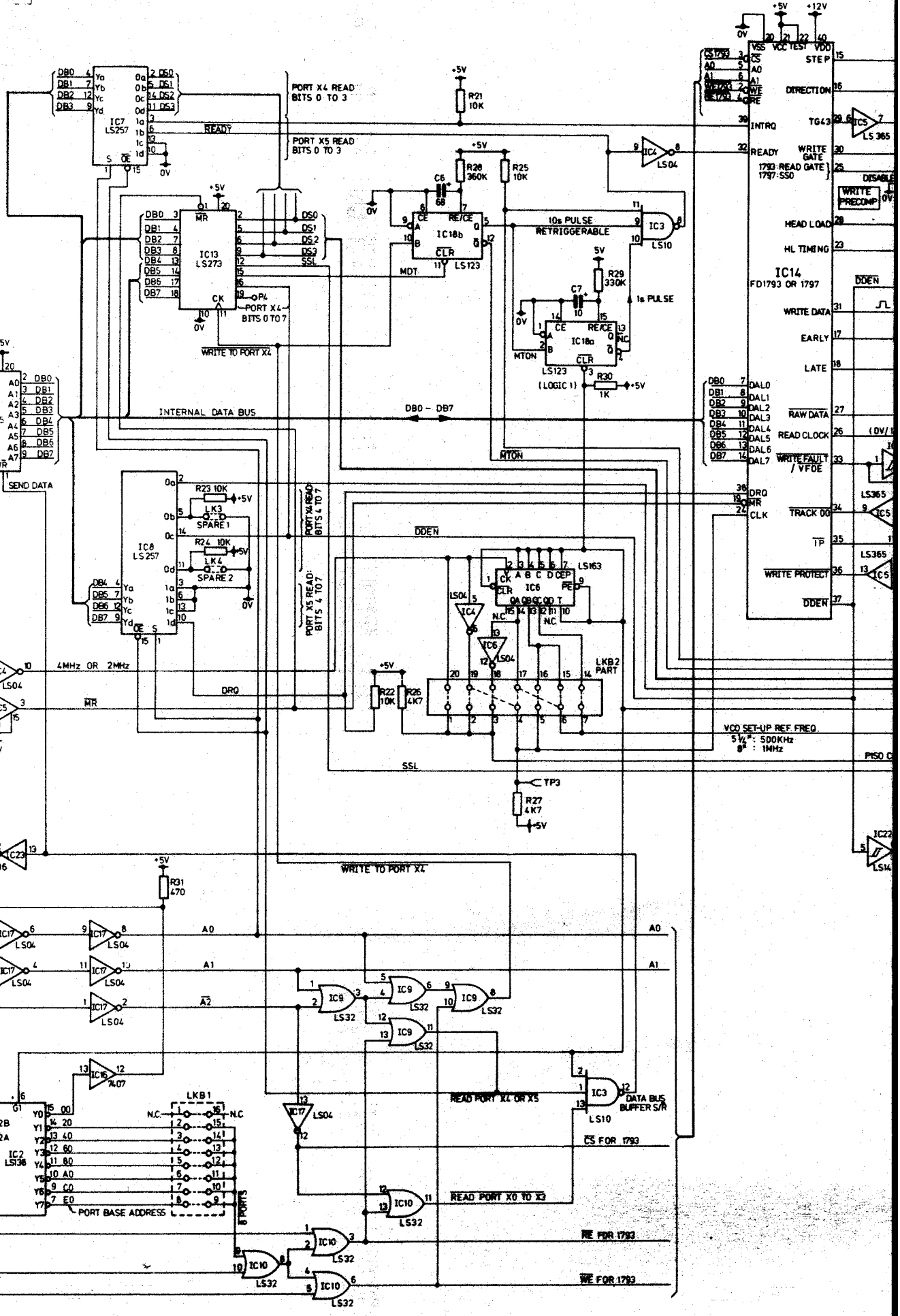
A6 36

A5 35

RD 29

IORD 26

WR 28



VCO SET-UP REF. FREQ
5 1/2" = 500KHz
8" = 1MHz

PSO D

IC22
LS14

WRITE TO PORT X4

A0

A1

A2

READ PORT X4 OR X5

DATA BUS BUFFER S/R

CS FOR 1793

READ PORT X0 TO X3

RE FOR 1793

WE FOR 1793